

File No.: 14836-8US-1 AD/mb

Montreal, Canada
March 9, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Applicant: MICROBRIDGE TECHNOLOGIES INC.
Serial No.: (unknown)
Filed: (unknown)
Title: METHOD FOR TRIMMING RESISTORS
Group Art Unit: --
Examiner: --
Agent of Record: James Anglehart Tel: (514) 847-4244

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INFORMATION DISCLOSURE STATEMENT
PRIOR TO FIRST OFFICE ACTION

Sir:

Pursuant to the duty of disclosure under 37 CFR 1.56, copies of the references listed on the attached PTO Form SB-08A-B are submitted herewith.

The references were cited in an International Search Report (ISR) mailed on **January 15, 2004** by the EPO in connection with a corresponding PCT application. A copy of the ISR is also enclosed.

In accordance with 37 CFR 1.97(h), the submission of the present information is not to be construed as an admission that such information is, or is considered to be material to patentability.

U.S. Patent and Trademark Office

The Examiner is kindly requested to consider these references during the examination of the above-identified application, making the references of record, and to return an initialed copy of the SB-08A-B Form to the below-signed agent.

Respectfully submitted,

MICROBRIDGE TECHNOLOGIES INC.

By:



James Anglehart

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Enc.: Copies of references and International Search Report
2 sets of SB-08A-B Form

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| | | | Application Number | | |
| | | | Filing Date | | |
| | | | First Named Inventor | Oleg GRUDIN et al. | |
| | | | Art Unit | | |
| Examiner Name | | | | | |
| Sheet | 1 | of | 2 | Attorney Docket Number | 14836-8US-1 AD/mb |

| U.S. PATENT DOCUMENTS | | | | | |
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| Examiner Initials* | Cite No. ¹ | Document Number | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear |
| | | Number – Kind Code ² (if known) | | | |
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| Examiner Initials* | Cite No. ¹ | Foreign Patent Document | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear | T ⁶ |
| | | Country Code ³ – Number ⁴ – Kind Code ⁵ (if known) | | | | |
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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria VA 22313-1450.

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| | | | First Named Inventor | Oleg GRUDIN et al. | |
| | | | Art Unit | | |
| Examiner Name | | | | | |
| Sheet | 2 | of | 2 | Attorney Docket Number | 14836-8US-1 AD/mb |

| NON PATENT LITERATURE DOCUMENTS | | | |
|---------------------------------|-----------------------|---|----------------|
| Examiner Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published | T ² |
| | | Pulse Current Trimming of Polysilicon Resistors, D.W. Feldbaumer et al., IEEE Transactions on Electron Devices, IEEE, N.Y., USA, Vol. 42, N° 4, pp. 689-696. | |
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